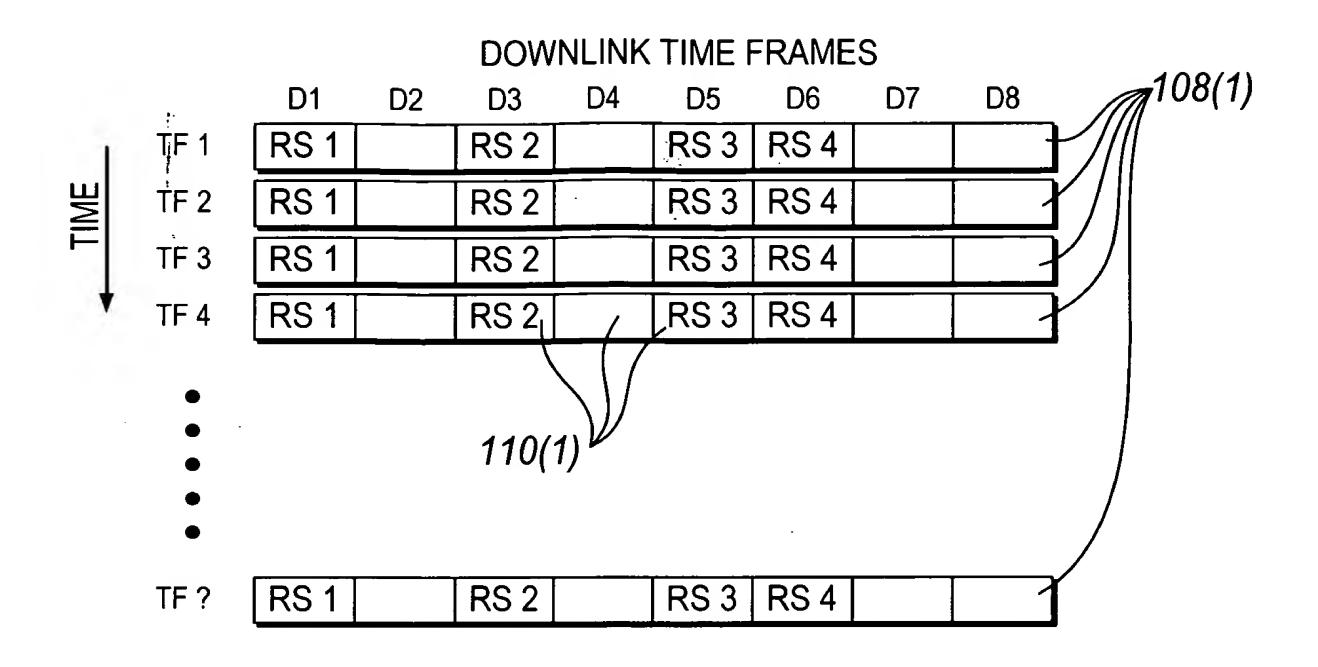


FIG. 1



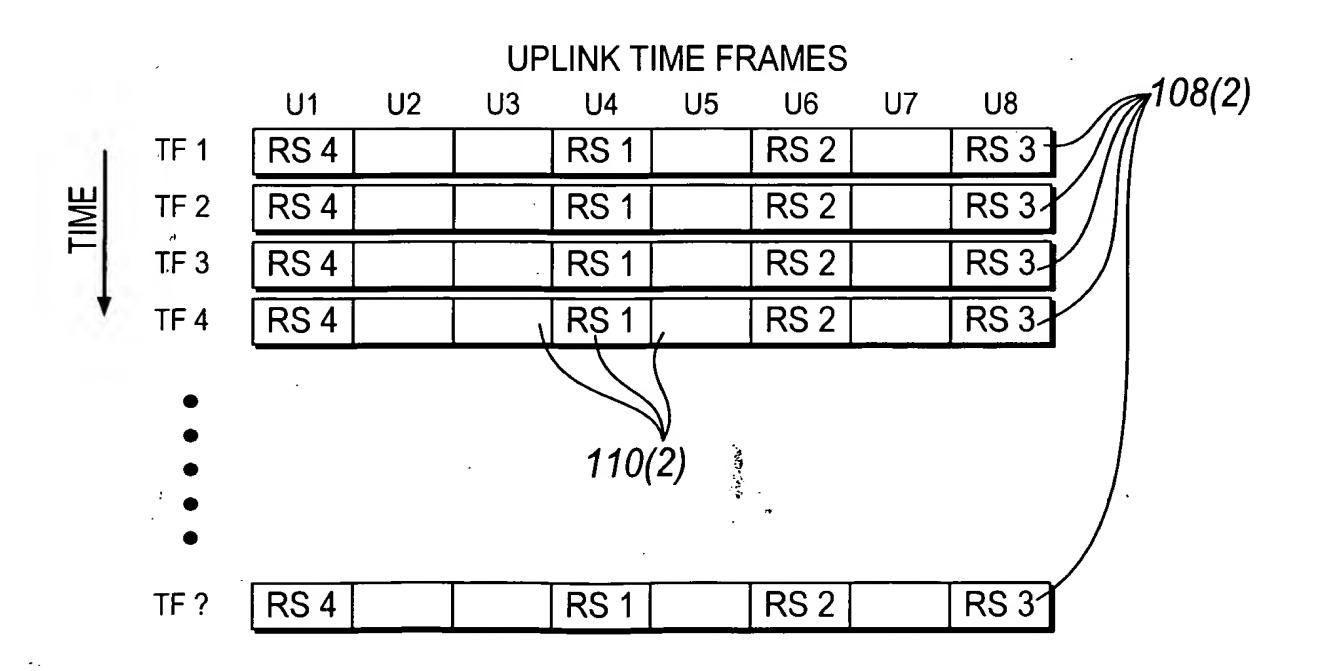


FIG. 2

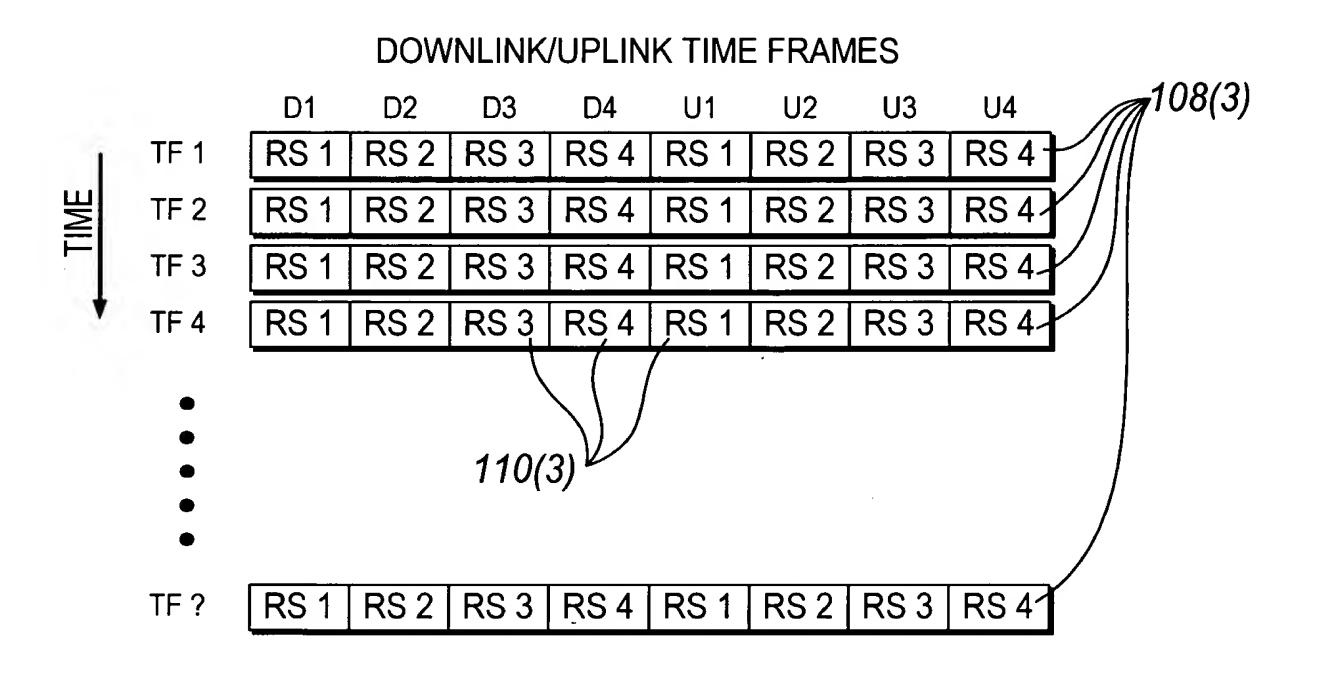


FIG. 3

FIG. 4A

3

FIG. 4B

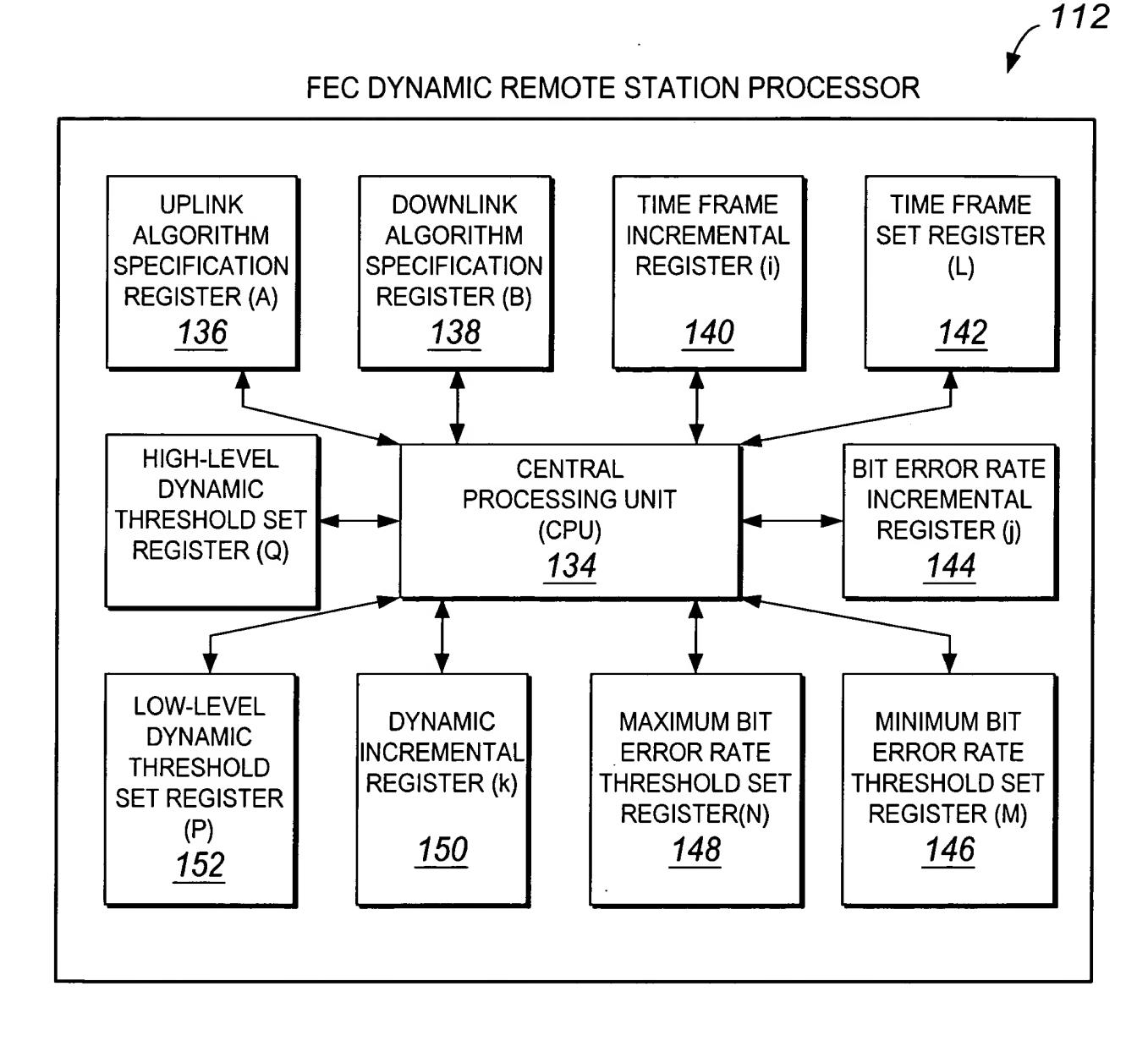


FIG. 5A

212

FEC DYNAMIC REMOTE STATION PROCESSOR **UPLINK** DOWNLINK TIME FRAME TIME FRAME **ALGORITHM ALGORITHM** SET REGISTER **INCREMENTAL** SPECIFICATION **SPECIFICATION** REGISTER (i) (L) REGISTER (A) REGISTER (B) HIGH-LEVEL BIT ERROR RATE **DYNAMIC CENTRAL INCREMENTAL** PROCESSING UNIT **THRESHOLD** REGISTER (j) **SET REGISTER** (CPU) (Q) FIRST-LEVEL BIT **SECOND-LEVEL** LOW-LEVEL BIT ERROR RATE **ERROR RATE DYNAMIC DYNAMIC** THRESHOLD SET THRESHOLD SET INCREMENTAL **THRESHOLD** REGISTER (M) REGISTER(N) SET REGISTER REGISTER (k) <u>246</u> <u>248</u> (P)

FIG. 5B

112'

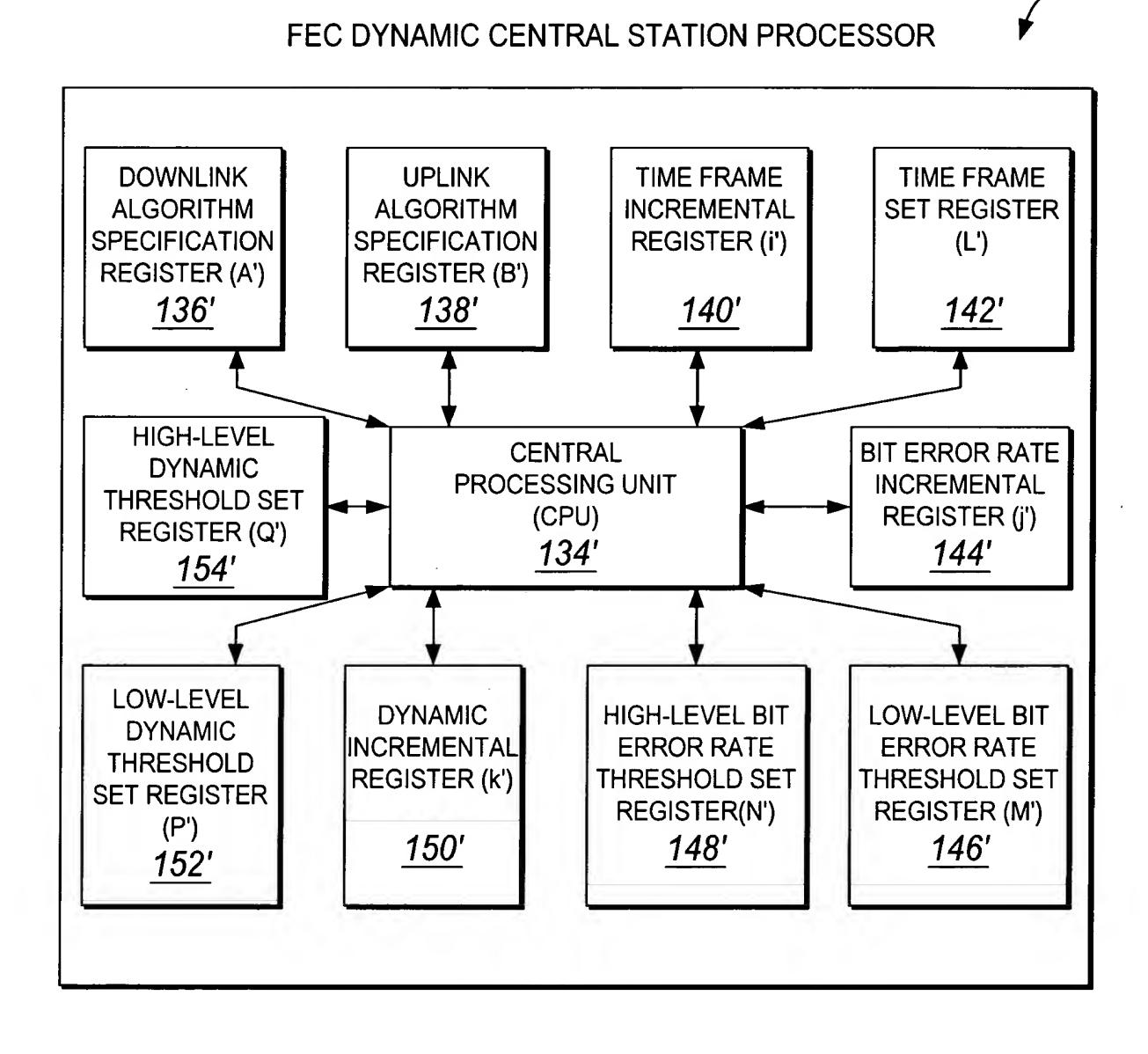


FIG. 6

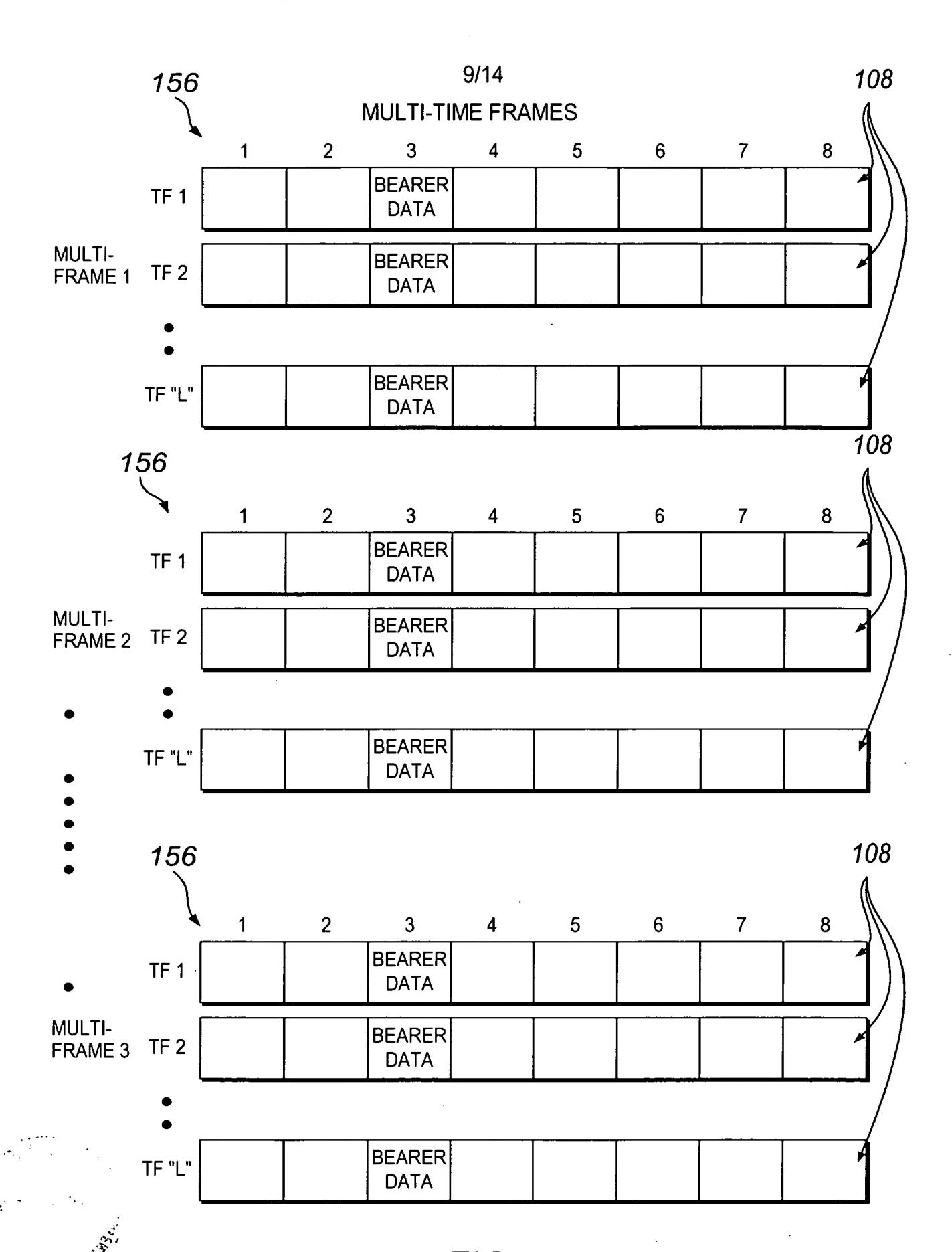


FIG. 7

FIG. 8

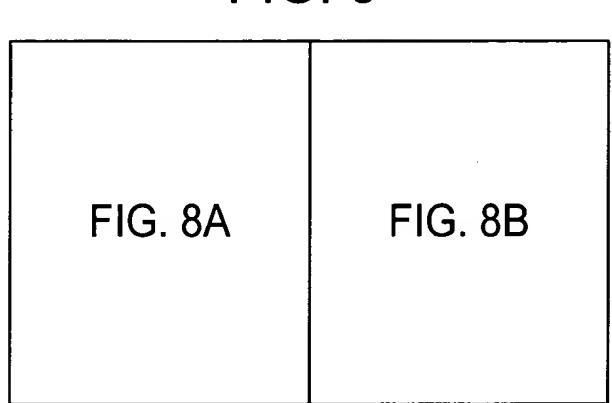


FIG. 9

FIG. 9A FIG. 9B

FIG. 8A

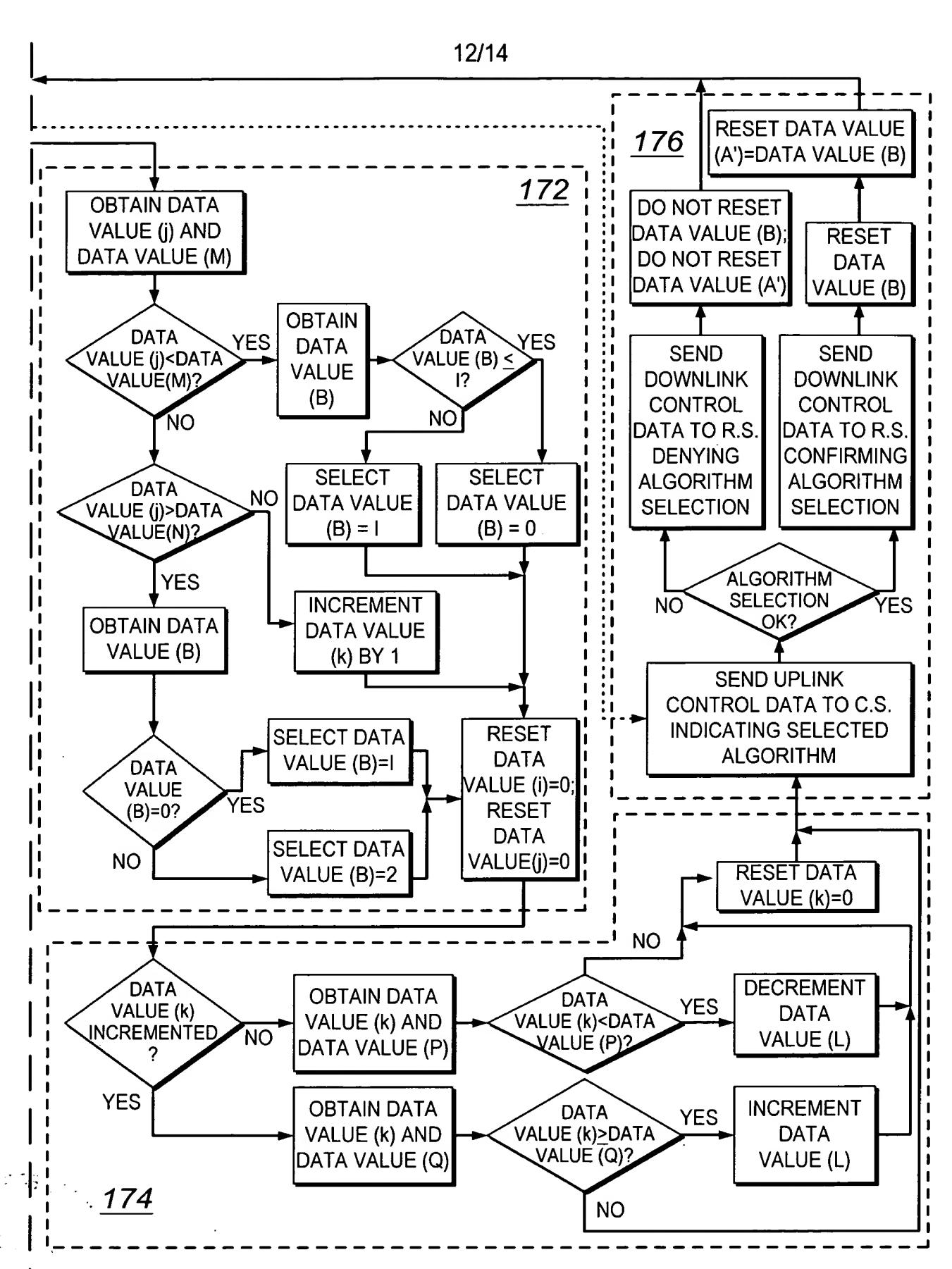
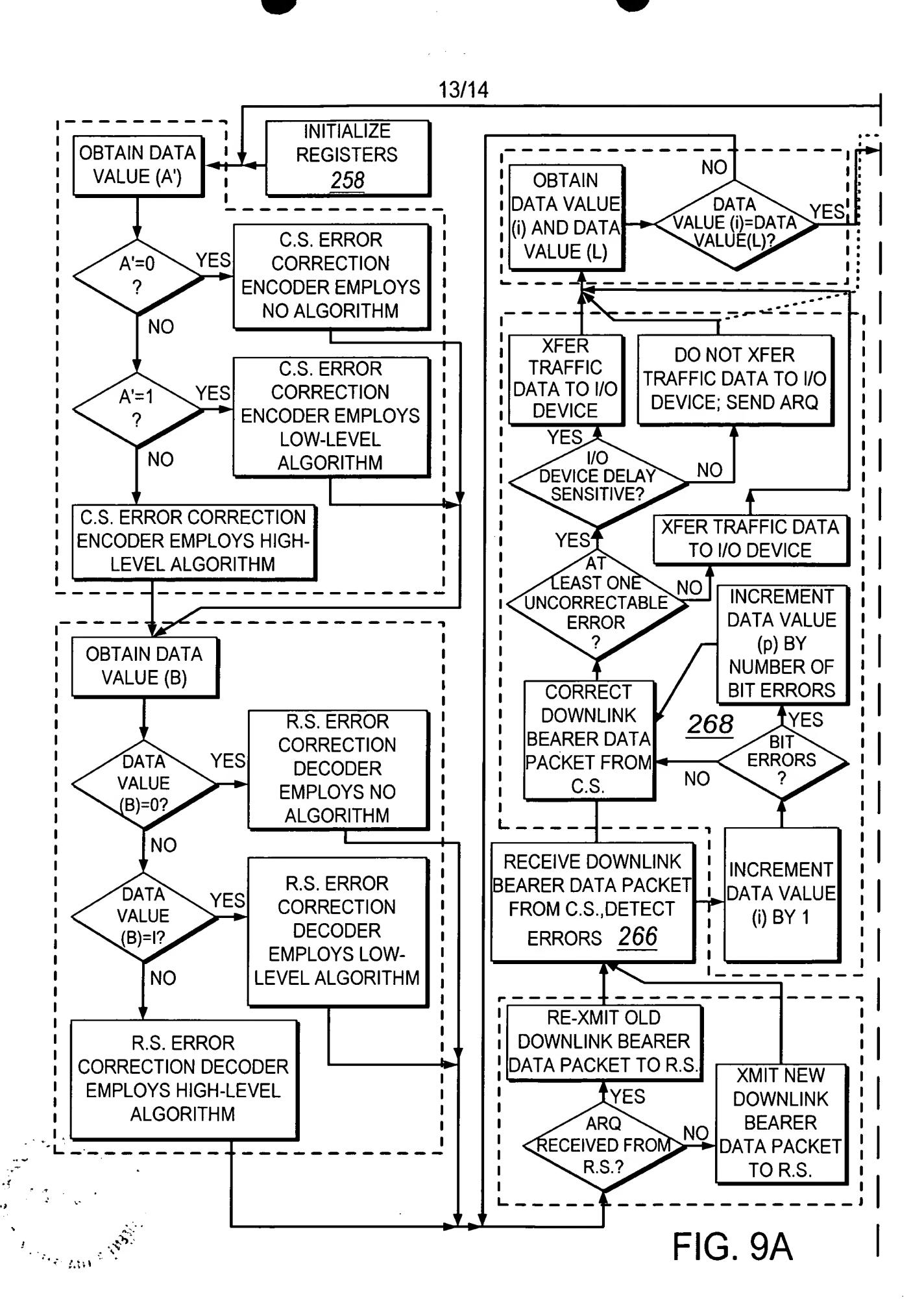


FIG. 8B





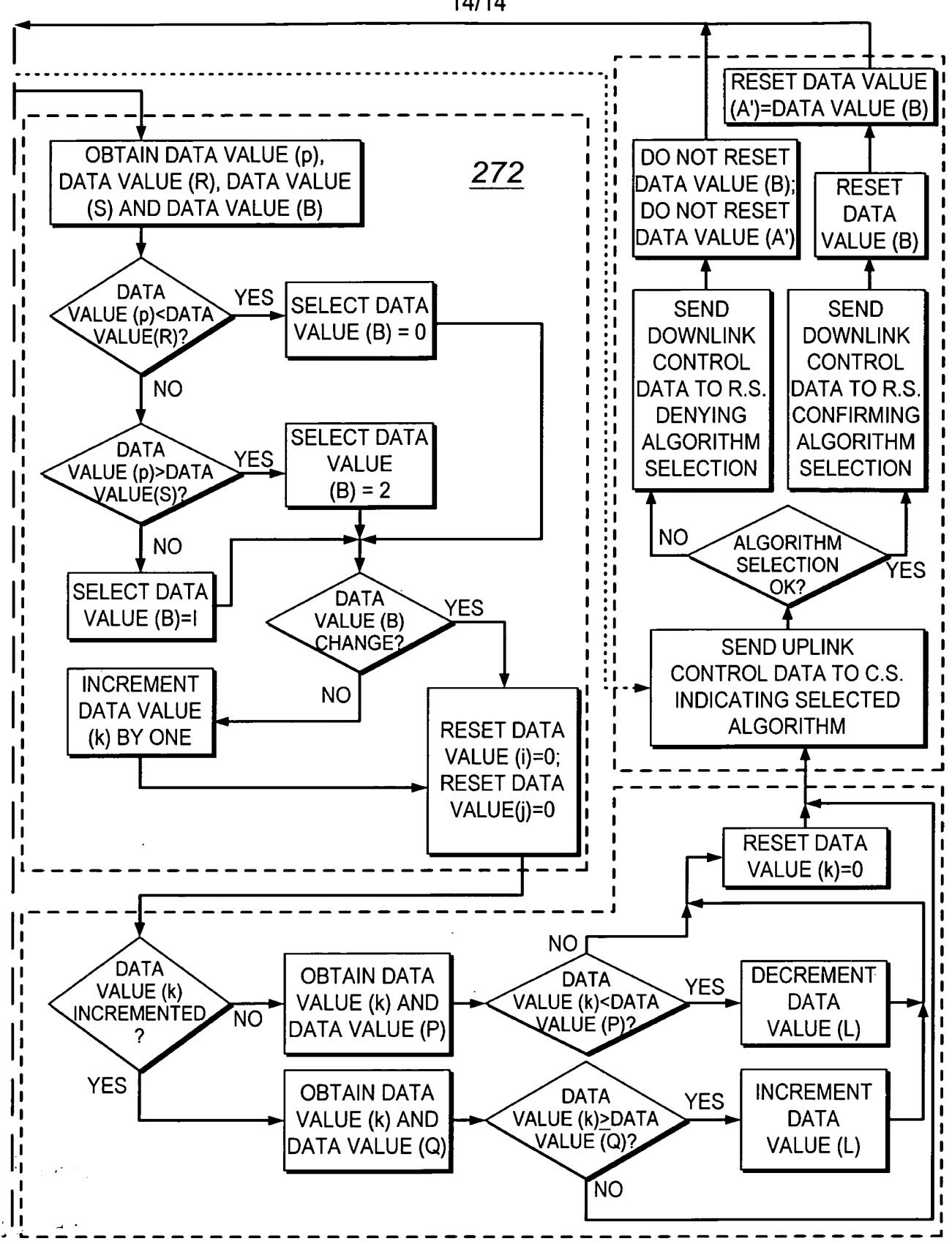


FIG. 9B